



00862.022498

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TAKAO YONEHARA, et al.

Application No.: 10/059,144

Filed: January 31, 2002

For: THIN-FILM SEMICONDUCTOR
DEVICE AND METHOD OF
MANUFACTURING THE SAME

Examiner: Not Yet Assigned

Group Art Unit: 2812

23

August 15, 2002

TECHNOLOGY CENTER 2800

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2-2203
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Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed below and on the enclosed Form PTO-1449. Copies of the below-listed documents are enclosed.

(1) European Patent 1,122,794

(2) European Patent 858,110

(3) European Patent 849,788

(4) Shimoda, T., et al., "Surface Free Technology By Laser Annealing (SUFTLA)", International Electron Devices Meeting 1999. IEDM. Technical Digest. Washington, DC, Dec. 5 to 8, 1999, New York, NY: IEEE, US, August 1, 1999 (1999-08-01), pages 289 to 292, XP000933199 ISBN: 0-7803-5411-7.

(5) Japanese Patent 11-316397

(6) U.S. Patent 6,190,937

(7) U.S. Patent 6,222,513

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

August 15 2002

(Date of Deposit)

Carole A. Quinn, Reg. No. 39,000

Attorney for Applicant

Signature

Date of Signature

August 15 2002

- (8) U.S. Patent 6,258,698
- (9) U.S. Patent 6,306,729
- (10) U.S. Patent 6,331,208
- (11) U.S. Patent 6,342,433
- (12) U.S. Patent 6,382,292

Documents (1) to (4) were cited in a European Search Report dated June 7, 2002 during prosecution of a related European patent application. A copy of the European Search Report is also enclosed. Document (7) issued from U.S. Application No. 09/037,767, which is believed to be a U.S. counterpart application to JP 11-316397 (i.e., Document (5) above). An English-language abstract of Document (5) is also enclosed.

The Examiner's attention is also directed to the following U.S. application, which is commonly assigned with the subject application:

<u>Application No.</u>	<u>Filing Date</u>	<u>Group Art Unit</u>
09/586,887	06/05/2000	2825

In accordance with 37 C.F.R. § 1.98(a)(2)(iii), a copy of the application, including specification, claims and drawings, is enclosed.

Finally and in an Information Disclosure Statement (IDS) filed concurrently with the subject application on January 31, 2002, the Examiner's attention was directed to two U.S. applications also filed on January 31, 2002, listed below.

<u>Application No.</u>	<u>Attorney Docket No.</u>	<u>Filing Date</u>	<u>Group Art Unit</u>
10/059,116	00862.022497	01/31/2002	2814
10/059,171	00862.022499	01/31/2002	2871

In accordance with 37 C.F.R. § 1.98(a)(2)(iii), copies of the applications, including specification, claims and drawings, were submitted with the January 31, 2002

IDS. Since these applications had not yet been assigned application serial numbers, they were identified in the January 31st IDS by their corresponding attorney docket numbers. Applicants now provide the application serial numbers and group art unit assignments corresponding to these two applications. In addition and for the Examiner's convenience, an additional copy of the specification, claims and drawings of each of these applications is provided herewith.

Inasmuch as this application has not yet received a first Office Action on the merits, it is believed that this Information Disclosure Statement is timely. See 37 C.F.R. §1.97(b)(3). Accordingly, the Examiner is urged to study this information in its entirety and to form an independent determination of the materiality of the information to the claimed invention. Additionally, the Examiner is requested to indicate that this information has been considered by initialing the appropriate portion of Form PTO-1449.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office by telephone at (714) 540-8700. All correspondence should continue to be directed to our address given below.

Respectfully submitted,


Attorney for Applicants

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AUG 19 2002

PRIORITY DOCKET NO. 00862.022498

APPLICATION NO. 10/059,144

APPLICANT **TAKAO YONEHARA, et al.**FILING DATE **January 31, 2002**GROUP **2812**

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,190,937	02/20/2001	Nauagawa, et al.	438	67	
		6,222,513	03/10/1998	Howard, et al.	345	84	
		6,258,698	07/10/2001	Iwasaki, et al.	438	455	
		6,306,729	10/23/2001	Sauaguchi, et al.	438	458	
		6,331,208	12/18/2001	Nishida, et al.	117	89	
		6,342,433	01/29/2002	Ohmi, et al.	438	455	
		6,382,292	05/07/2002	Ohmi, et al.	156	584	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
	EP	1 122 794	08/08/2001	Europe			
	EP	858 110	08/12/1998	Europe			
	EP	849 788	06/24/1998	Europe			
	JP	11-316397	11/16/1999	Japan	G02F		Abstract

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

Shimoda, T., et al, "Surface Free Technology By Laser Annealing (SUFTLA)" International Electron Devices Meeting 1999. IEDM. Technical Digest. Washington, DC, Dec. 5 to 8, 1999, New York, NY: IEEE, US, August 1, 1999 (1999-08-01), pages 289 to 292, XP000933199 ISBN: 0-7803-5411-7.

EXAMINER

DATE CONSIDERED

XAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 1 of 1



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**Europäisches
Patentamt**

Zweigstelle
in Den Haag
Recherchen-
abteilung

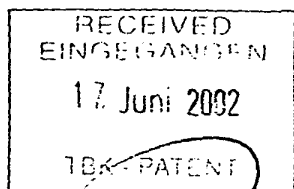
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Département à
La Haye
Division de la
recherche

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Datum/Date
07.06.02

Zeichen/Ref./Réf. EP 33523	Anmeldung Nr./Application No./Demande n°/Patent Nr./Patent No./Brevet n°. 02002257.0-2205-
Anmelder/Applicant/Demandeur/Patentinhaber/Propriétaire/Titulaire CANON KABUSHIKI KAISHA	

COMMUNICATION

The European Patent Office herewith transmits as an enclosure the European search report for the above-mentioned European patent application.

If applicable, copies of the documents cited in the European search report are attached.

☒ Additional set(s) of copies of the documents cited in the European search report is (are) enclosed as well.

The following specifications given by the applicant have been approved by the Search Division:

☐ abstract

☐ title

☒ The abstract was modified by the Search Division and the definitive text is attached to this communication.

The following figure will be published together with the abstract: **NONE**

REFUND OF THE SEARCH FEE

If applicable under Article 10 Rules relating to fees, a separate communication from the Receiving Section on the refund of the search fee will be sent later.





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X,P	EP 1 122 794 A (SEMICONDUCTOR ENERGY LAB) 8 August 2001 (2001-08-08) * figures 1,7C,8C * * paragraphs '0032!,'0039!-'0041!,'0049!-'0052!,'0088!, '0094!,'0095!,'0098!,'0103! *	1,7,8,10	G02F1/1362
X	EP 0 858 110 A (SEIKO EPSON CORP) 12 August 1998 (1998-08-12) * column 40, line 7 - column 45, line 12 * * figures 17-22,36 *	10	
A		1,9	
X	EP 0 849 788 A (CANON KK) 24 June 1998 (1998-06-24) * page 12, line 27 - line 56 * * figure 4 *	10	
A		1,2,4-6,9	
X	SHIMODA T ET AL: "SURFACE FREE TECHNOLOGY BY LASER ANNEALING (SUFTLA)" INTERNATIONAL ELECTRON DEVICES MEETING 1999. IEDM. TECHNICAL DIGEST. WASHINGTON, DC, DEC. 5 - 8, 1999, NEW YORK, NY: IEEE, US, 1 August 1999 (1999-08-01), pages 289-292, XP000933199 ISBN: 0-7803-5411-7 * the whole document *	10	TECHNICAL FIELDS SEARCHED (Int.Cl.7) G02F H01L
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 7 May 2002	Examiner Hauser, M
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 02 00 2257

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

07-05-2002

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 1122794 A	08-08-2001	CN 1312590 A	12-09-2001
		EP 1122794 A2	08-08-2001
		JP 2001290439 A	19-10-2001
		US 2001040645 A1	15-11-2001
EP 0858110 A	12-08-1998	JP 10125929 A	15-05-1998
		JP 10125930 A	15-05-1998
		JP 10125931 A	15-05-1998
		JP 11026734 A	29-01-1999
		EP 0858110 A1	12-08-1998
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		CN 1199507 A	18-11-1998
		WO 9809333 A1	05-03-1998
		JP 11074533 A	16-03-1999
		US 6127199 A	03-10-2000
EP 0849788 A	24-06-1998	AU 743331 B2	24-01-2002
		AU 4846297 A	25-06-1998
		CA 2225131 A1	18-06-1998
		CN 1190248 A	12-08-1998
		EP 0849788 A2	24-06-1998
		JP 10233352 A	02-09-1998
		SG 67458 A1	21-09-1999
		TW 410477 B	01-11-2000
		US 6100166 A	08-08-2000



ABSTRACT / ZUSAMMENFASSUNG / ABREGE

02002257.0

A thin-film semiconductor device used for a display region (111) and peripheral circuit region (112) and a method of manufacturing the same are provided. A method of manufacturing a display device includes the step of preparing a member (130) having, on a separation layer (100), a semiconductor film (110) having a first region (111) with a switching element (109) and a second region (112) with a peripheral circuit (102), the step of forming an image display portion (113) on the first region, and the separation step of separating the first and second regions from the member at the separation layer (100).